

Yitai Hu 202.408.4203

July 25, 2003

ATTORNEY DOCKET NO. 06720.0103 CUSTOMER NUMBER: 22,852

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Re: New U.S. Patent Application

Title: MIXED VOLTAGE CMOS I/O BUFFER WITH THIN OXIDE DEVICE AND DYNAMIC

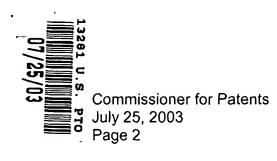
N-WELL BIAS CIRCUIT

Inventor(s): MING-DOU KER ET AL.

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

- 1. Application transmittal, 3 pages
- 2. Specification, 29 pages with Abstract, including 4 independent claims and 36 claims total.
- 3. Drawings 5 sheets of drawings containing six (6) figures (Figures 1-5B).
- 4. Declaration/Power of Attorney
- 5. Recordation Form Cover Sheet Assignment to Industrial Technology Research Institute



The filing fee is calculated as follows:

Basic Application Filing Fee					\$750	\$	750.00
	Number of Claims		Basic	Extra Claims			
Total Claims	36	-	20	16	x \$18		288.00
Independent Claims	4	-	3	1	x \$84		84.00
[] Presentation of Multiple Dep. Claim(s) +\$270							
Subtotal						\$	
Reduction by 1/2 if small entity						-	
TOTAL APPLICATION FILING FEE						\$	1122.

- 6. A check for \$1162.00 is enclosed. The fee includes:
 - \$ 750.00 filing fee;
 - \$ 372.00 extra claim fee;
 - \$ 40.00 Recordation Fee

Please address all correspondence with respect to this application to:

Finnegan, Henderson, Farabow, Garrett & Dunner, L.L.P. 1300 I Street, N.W. Washington, D.C. 20005-3315

Please accord this application a serial number and filing date.

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The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or § 1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

By: WW 2 October Dag 24,014
For Yitai Hu

Reg. No. 40,653

YH:pwl Enclosures